

IN THE CLAIMS:

The text of all pending claims, (including withdrawn claims) is set forth below. Cancelled and not entered claims are indicated with claim number and status only. The claims as listed below show added text with underlining and deleted text with ~~strikethrough~~. The status of each claim is indicated with one of (original), (currently amended), (cancelled), (withdrawn), (new), (previously presented), or (not entered).

Please AMEND claims 1, 3, 39, 41 and 44 in accordance with the following:

1 **(currently amended)** A combined DVD (Digital Video Disk)/CD (Compact Disk) data processor to process a pulse stream of data from a DVD or a CD, comprising:

a demodulator to EFM+ demodulate the pulse stream in a DVD mode, and EFM demodulate the pulse stream in a CD mode, to generate demodulated data;

~~an~~ a single ECC (error checking and correction) decoder to error-correct the demodulated data ~~stored~~ according to a predetermined code length and error correction range, the predetermined code length and error correction range having different values in the DVD and CD modes, to generate error corrected data; and

a memory to store the error corrected data from said ECC decoder, wherein said memory comprises a first memory map including a plurality of blocks of the error corrected data each having a first fixed number of bytes in the DVD mode, and a second memory map including a plurality of frames of the error corrected data each having a second fixed number of bytes in the CD mode.

2. **(previously presented)** The combined DVD/CD data processor as claimed in claim 1, wherein the predetermined code length and error correction range in the DVD mode are PI(182,172), PO(208,192), and the predetermined code length and error correction range in the CD mode are C1(32,28), C2(28,24).

3. **(currently amended)** ~~The combined DVD/CD data processor as claimed in claim 2,~~
A combined DVD (Digital Video Disk)/CD (Compact Disk) data processor to process a pulse stream of data from a DVD or a CD, comprising:

a demodulator to EFM+ demodulate the pulse stream in a DVD mode, and EFM demodulate the pulse stream in a CD mode, to generate demodulated data;

an ECC (error checking and correction) decoder to error-correct the demodulated data

stored according to a predetermined code length and error correction range, the predetermined code length and error correction range having different values in the DVD and CD modes, to generate error corrected data; and

a memory to store the demodulated data and the error corrected data, wherein said memory comprises a first memory map including a plurality of blocks of the error corrected data each having a first fixed number of bytes in the DVD mode, and a second memory map including a plurality of frames of the error corrected data each having a second fixed number of bytes in the CD mode,

wherein said ECC decoder comprises:

a syndrome generator to receive said demodulated data from said memory to generate a syndrome polynomial according to said a code length and correction range of PI(182,172), PO(208,192) in the DVD mode, and of C1(32,28), C2(28,24) in the CD mode;

an erasure constant generator to receive an erasure flag to generate an erasure constant;

a modified syndrome calculator to receive the syndrome polynomial and the erasure constant to calculate a modified syndrome and generate a Forney syndrome polynomial and an erasure polynomial;

a modified Euclidean algorithm to process the Forney syndrome polynomial and the erasure polynomial based on a modified Euclidean algorithm, to generate an errata locator polynomial and an errata evaluator polynomial; and

a Chien search and error correction circuit to correct errors of the demodulated data stored in said memory according to said errata locator polynomial and said errata evaluator polynomial.

4. **(original)** The combined DVD/CD data processor as claimed in claim 1, wherein said memory is an external memory.

5. **(previously presented)** A combined DVD (Digital Video Disk)/CD (Compact Disk) data processor to process a pulse stream of data from a DVD or a CD, comprising:

a PLL (Phase Locked Loop) to receive the pulse stream, to generate a PLL clock;

a frame/ID (identification) synchronization detector to latch the pulse stream according to said PLL clock, to generate a symbol clock;

a single demodulator to EFM+ demodulate the pulse stream according to the symbol

clock in a DVD mode, and EFM demodulate the pulse stream according to the symbol clock in a CD mode, to generate demodulated data;

a memory to store the demodulated data from said demodulator;

an ECC (error checking and correction) decoder to error-correct the demodulated data stored in said memory according to a predetermined code length and error correction range, the predetermined code length and error correction range having different values in the DVD and CD modes, to generate error corrected data;

a descrambler to descramble the error corrected data stored in said memory, in the DVD mode; and

a CD audio processor to process the error corrected data stored in said memory, in the CD mode, wherein:

said memory comprises a first memory map including a plurality of blocks of the error corrected data each having a first fixed number of bytes in the DVD mode, and a second memory map including a plurality of frames of the error corrected data each having a second fixed number of bytes in the CD mode;.

6. **(original)** The combined DVD/CD processor as claimed in claim 5, wherein:

the plurality of blocks is 13;

the first fixed number of bytes is 32.25 Kbytes;

the plurality of frames is 256; and

the second fixed number of bytes is 32 bytes.

7. **(cancelled)**

8. **(previously presented)** A combined DVD (Digital Video Disk)/CD (Compact Disk) data processor to process first and second pulse streams from a DVD and a CD, respectively, comprising:

a PLL (Phase Locked Loop) to receive the first and second pulse streams, to generate respective first and second PLL clocks;

a frame/ID (identification) synchronization detector to latch the first and second pulse streams according to the respective first and second PLL clocks, to generate respective first and second symbol clocks;

a single demodulator to perform a first type of demodulation on the first pulse stream

according to the first symbol clock to generate first demodulated data of a DVD mode, and a second type of demodulation on the second pulse stream according to the second symbol clock to generate second demodulated data of a CD mode;

a memory to store the first and second demodulated data; and

an ECC (error checking and correction) decoder to error correct the first demodulated data stored in said memory in accordance with a first predetermined code length and error correction range and to store the error corrected first demodulated data back in said memory, and to error correct the second demodulated data stored in said memory in accordance with a second predetermined code length and error correction range and to store the error corrected second demodulated data back in said memory;

wherein said memory comprises:

a first memory map to store the error corrected first demodulated data; and

a second memory map different from the first memory map, to store the error corrected second demodulated data.

9. **(original)** The combined DVD/CD data processor as claimed in claim 8, wherein said first memory map provides a VBR (variable bit rate) control margin to interface the error corrected first demodulated data with an audio/video decoder.

10. **(previously presented)** The combined DVD/CD data processor as claimed in claim 8, wherein said ECC decoder comprises:

a syndrome generator to generate syndrome polynomials from the first and second demodulated data stored in said memory in accordance with the corresponding first and second code lengths and corresponding first and second correction ranges;

an erasure constant generator to generate first and second erasure constants from corresponding first and second erasure flags;

a modified syndrome calculator to generate first and second Forney syndrome polynomials and first and second erasure polynomials from the corresponding first and second erasure constants and the corresponding syndrome polynomials;

a modified Euclidean algorithm to process the first and second Forney syndrome polynomials with the corresponding first and second erasure polynomials, to generate corresponding first and second errata locator polynomials and corresponding first and second errata evaluator polynomials; and

a Chien search and error correction unit to correct errors of the first and second demodulated data stored in said memory according to the corresponding first and second errata locator polynomials and the corresponding first and second errata evaluator polynomials.

11. **(previously presented)** The combined DVD/CD data processor as claimed in claim 8, further comprising:

a descrambler to descramble the error corrected first demodulated data stored in said memory for use with an audio/video decoder; and

an audio processor to audio process the error corrected second demodulated data stored in said memory.

12. **(cancelled)**

13. **(previously presented)** A combined DVD (Digital Video Disk)/CD (Compact Disk) data processor to process first and second pulse streams from a DVD and a CD, respectively, comprising:

a single demodulator to demodulate the first and second pulse streams in a DVD mode and a CD mode, respectively, to generate first and second demodulated data, respectively;

a memory to store the first and second demodulated data; and

an ECC (error checking and correction) decoder to error correct the first demodulated data stored in said memory in accordance with a first predetermined code length and error correction range in the DVD mode, and to error correct the second demodulated data stored in said memory in accordance with a second predetermined code length and error correction range in the CD mode, wherein:

said memory stores the error corrected first demodulated data output from said ECC decoder in a first memory map, and the error corrected second demodulated data output from said ECC decoder in a second memory map different from the first memory map.

14. **(previously presented)** The combined DVD/CD data processor as claimed in claim 13, further comprising:

a descrambler to descramble the error corrected first demodulated data stored in said memory for use with an audio/video decoder; and

an audio processor to audio process the error corrected second demodulated data

stored in said memory.

15. **(previously presented)** The combined DVD/CD data processor as claimed in claim 13, further comprising:

a PLL (Phase Locked Loop) to receive the first and second pulse streams, to generate respective first and second PLL clocks; and

a frame/ID (identification) synchronization detector to latch the first and second pulse streams according to the respective first and second PLL clocks, to generate respective first and second symbol clocks;

wherein said demodulator performs a first type of demodulation on the first pulse stream according to the first symbol clock to generate the first demodulated data in the DVD mode, and a second type of demodulation on the second pulse stream according to the second symbol clock to generate the second demodulated data in the CD mode.

16. **(cancelled)**

17. **(previously presented)** A combined DVD (Digital Video Disk)/CD (Compact Disk) data processor to process first and second pulse streams from a DVD and a CD, respectively, comprising:

a demodulator to demodulate the first and second pulse streams in a DVD mode and a CD mode, respectively, to generate first and second demodulated data, respectively;

a single external memory to store the first and second demodulated data; and

a single ECC (error checking and correction) decoder to error correct the first and second demodulated data stored in said memory, wherein said single ECC decoder error corrects the first and second demodulated data stored in said memory in accordance with corresponding different code lengths and correction ranges; wherein:

said memory comprises a first memory map including a plurality of blocks of the error corrected data each having a first fixed number of bytes in the DVD mode, and a second memory map including a plurality of frames of the error corrected data each having a second fixed number of bytes in the CD mode.

18. **(previously presented)** The combined DVD/CD data processor as claimed in claim 17, further comprising:

a descrambler to descramble the error corrected first demodulated data for use with an audio/video decoder; and

an audio processor to audio process the error corrected second demodulated data.

19. **(previously presented)** The combined DVD/CD data processor as claimed in claim 17, further comprising:

a PLL (Phase Locked Loop) to receive the first and second pulse streams, to generate respective first and second PLL clocks; and

a frame/ID (identification) synchronization detector to latch the first and second pulse streams according to the respective first and second PLL clocks, to generate respective first and second symbol clocks;

wherein said demodulator performs a first type of demodulation on the first pulse stream according to the first symbol clock to generate the first demodulated data in a DVD mode, and a second type of demodulation on the second pulse stream according to the second symbol clock to generate the second demodulated data in a CD mode.

20 -21 **(cancelled)**

22. **(previously presented)** A data processor apparatus for common use in a DVD (Digital Video Disk)/CD (Compact Disk) player using discrimination information provided according to a DVD or a CD, comprising:

a single pre-processor generating a clock from a pulse stream read from one of the DVD and the CD and performing demodulation of the pulse stream according to the discrimination information and the generated clock;

a memory unit storing the demodulated pulse stream processed by the single pre-processor as data in a corresponding format according to the discrimination information; and

a data processor and converter processing the data stored in the memory unit;

wherein:

the data processor and converter comprises an error corrector error-correcting the data stored in the memory unit according to the discrimination information using a preset error correcting method; and

the data processor and converter audio-converts or data-converts the processed data according to the discrimination information; and

the memory comprises a first memory map including a plurality of blocks of the error corrected data each having a first fixed number of bytes in a DVD mode, and a second memory map including a plurality of frames of the error corrected data each having a second fixed number of bytes in a CD mode.

23. **(previously presented)** The data processor apparatus as claimed in claim 22, wherein the preset error correcting method of the error corrector depends on a code length and a correcting range according to the discrimination information.

24. **(previously presented)** The data processor apparatus as claimed in claim 22, wherein the memory unit comprises:

a data storage memory; and

a memory controller controlling the memory to store the data in the corresponding format according to the discrimination information.

25 -38 **(cancelled)**

39. **(currently amended)** A combined DVD (Digital Video Disk)/CD (Compact Disk) data processor to process a pulse stream of data from a DVD or a CD, comprising:

a PLL (Phase Locked Loop) to receive the pulse stream, to generate a PLL clock;

a frame/ID (identification) synchronization detector to latch the pulse stream according to said PLL clock, to generate a symbol clock;

a demodulator to EFM+ demodulate the pulse stream according to the symbol clock in a DVD mode, and EFM demodulate the pulse stream according to the symbol clock in a CD mode, to generate demodulated data;

a memory to store the demodulated data from said demodulator;

~~an a~~ single ECC (error checking and correction) decoder to error-correct the demodulated data stored in said memory according to a predetermined code length and error correction range, the predetermined code length and error correction range having different values in the DVD and CD modes, to generate error corrected data;

a descrambler to descramble the error corrected data stored in said memory, in the DVD mode; and

a CD audio processor to process the error corrected data stored in said memory, in the CD mode,

wherein said memory has a first memory map including a plurality of blocks of the error corrected data each having a first fixed number of bytes in the DVD mode, and a second memory map including a plurality of frames of the error corrected data each having a second fixed number of bytes in the CD mode.

40. **(previously presented)** The combined DVD/CD processor as claimed in claim 39, wherein:

the plurality of blocks is 13;

the first fixed number of bytes is 32.25 Kbytes;

the plurality of frames is 256; and

the second fixed number of bytes is 32 bytes.

41. **(currently amended)** A combined DVD (Digital Video Disk)/CD (Compact Disk) data processor to process first and second pulse streams from a DVD and a CD, respectively, comprising:

a PLL (Phase Locked Loop) to receive the first and second pulse streams, to generate respective first and second PLL clocks;

a frame/ID (identification) synchronization detector to latch the first and second pulse streams according to the respective first and second PLL clocks, to generate respective first and second symbol clocks;

a demodulator to perform a first type of demodulation on the first pulse stream according to the first symbol clock to generate first demodulated data of a DVD mode, and a second type of demodulation on the second pulse stream according to the second symbol clock to generate second demodulated data of a CD mode;

a memory to store the first and second demodulated data; and

~~an a single~~ ECC (error checking and correction) decoder to error correct the first demodulated data stored in said memory in accordance with a first predetermined code length and error correction range and to store the error corrected first demodulated data back in said memory, and to error correct the second demodulated data stored in said memory in accordance with a second predetermined code length and error correction range and to store the error corrected second demodulated data back in said memory, wherein said memory comprises:

- a first memory map to store the error corrected second demodulated data; and
- a second memory map different from the first memory map, to store the error corrected second demodulated data.

42. **(previously presented)** The combined DVD/CD data processor as claimed in claim 41, wherein said first memory map provides a VBR (variable bit rate) control margin to interface the error corrected first demodulated data with an audio/video decoder.

43. **(cancelled)**

44. **(currently amended)** A combined DVD (Digital Video Disk)/CD (Compact Disk) data processor to process first and second pulse streams from a DVD and a CD, respectively, comprising:

- a demodulator to demodulate the first and second pulse streams in a DVD mode and a CD mode, respectively, to generate first and second demodulated data, respectively;

- a memory to store the first and second demodulated data; and

- ~~an a single~~ ECC (error checking and correction) decoder to error correct the first demodulated data stored in said memory in accordance with a first predetermined code length and error correction range in the DVD mode, and to error correct the second demodulated data stored in said memory in accordance with a second predetermined code length and error correction range in the CD mode,

- wherein said memory stores the error corrected first demodulated data output from said ECC decoder in a first memory map, and the error corrected second demodulated data output from said ECC decoder in a second memory map different from the first memory map.

45. **(cancelled)**

46. **(previously presented)** A combined DVD (Digital Video Disk)/CD (Compact Disk) data processor to process first and second pulse streams from a DVD and a CD, respectively, comprising:

a single demodulator to demodulate the first and second pulse streams in a DVD mode and a CD mode, respectively, to generate first and second demodulated data, respectively;

an external memory to store the first and second demodulated data; and

a single ECC (error checking and correction) decoder to error correct the first and second demodulated data stored in said memory;

wherein:

said single ECC decoder error corrects the first and second demodulated data stored in said memory in accordance with corresponding different code lengths and correction ranges, and

said memory comprises a first memory map including a plurality of blocks of the first error corrected demodulated data each having a first fixed number of bytes in a DVD mode, and a second memory map including a plurality of frames of the error corrected data each having a second fixed number of bytes in the CD mode.

47. **(previously presented)** The combined DVD/CD data processor as claimed in claim 46, further comprising:

a descrambler to descramble the error corrected first demodulated data for use with an audio/video decoder; and

an audio processor to audio process the error corrected second demodulated data.

48. **(cancelled)**